



The Bombay Salesian Society's
Don Bosco Institute of Technology
(An Autonomous Institute Affiliated to University of Mumbai)

Date: 27/10/2025

Department of Electronics and Telecommunication Engineering

Time Table for Mid Semester Examination (MSE): ▽SE

AY (2025-2026)

SE SEMESTER III

Day & Date	Time	Course Code	Course Name
Monday, 03 rd Nov 2025	10:00 am - 11:30 am	25ET3PCC01	Mathematics for Signal Processing
Tuesday, 04 th Nov 2025	10:00 am - 11:30 am	25ET3PCC02	Linear Integrated Circuits
Wednesday, 05 th Nov 2025	10:00 am - 11:30 am	25ET3PCC03	Digital System Design using Verilog
Thursday, 06 th Nov 2025	10:00 am - 11:30 am	25ET3PCC04	Analog and Digital Communication

Revathy
27/10/2025

Dr. Revathy Sundararajan
Controller of Examinations

Sudhakar Mande

Dr. Sudhakar Mande
Principal